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~~Book M Morris Mano indexQ. 1.20: Convert decimal 49 and 29 to binary, using the signed 2's complement representation and e Q. 1.33: The state of a 12bit register is 10010010111. What is its content if it represents~~  
~~Boolean Logic \u0026 Logic Gates: Crash Course Computer Science #30. 6.24: Design a counter with T flipflops that goes through the following binary repeated sequence Q. 3.11: Draw a NAND logic diagram that implements the complement of the following function~~  
~~One MUST READ book on Digital Electronics | Digital Logic and Computer Design | Video in HindiQ. 3.15: Simplify the following Boolean function F, together with the don't-care conditions d, and Q. 1.25: Represent the decimal number 513 in (a)BCD (b) excess 3 code (c) 2421 code (d) 8421 code Q. 2.6: Find the minterms of the following Boolean expressions by plotting each function in a map Q. 2.19: Express following function as sum of minterms and product of maxterms: F= B'D + A'D + BD Q. 3.6: Simplify the following Boolean expressions, using four-variable maps: (a) A'B'C'D' + AC'D' + Q. 1.15: Find the 9's and the 10's complement of the following decimal numbers: (a) 52,784,630 Q. 3.4: Simplify the following Boolean functions, using Karnaugh maps: (a) F (x,y,z) = sum(2,3,6,7)~~  
~~Q. 3.13: Simplify the following expressions to (1) sum-of-products and (2) products-of-sumsQ. 4.4: Design a combinational circuit with three inputs and one output.(a) The output is 1 when Q. 3.12: Simplify the following Boolean functions to product-of-sums form: (a) F(w,x,y,z)=sum(0,1,2, Binary Addition and Subtraction With Negative Numbers, 2's Complements \u0026 Signed Magnitude~~  
~~Q. 4.1: Consider the combinational circuit shown in Fig. P4.1.(a)\* Derive the Boolean expressions fo~~  
~~12 Introduction to combinational circuitDigital Design| Q. 1.13: Do the following conversion problems: (a) Convert decimal 27.315 to binary~~  
~~Q. 3.14: Give three possible ways to express following Boolean function with eight or fewer literalsDigital Design by MORRIS MANO.flv Q. 3.16: Simplify the following functions, and implement them with two-level NAND gate circuits: Q. 3.9: Find all the prime implicants for the following Boolean functions, and determine which are~~  
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